

What is claimed is:

1 1. A pixel structure of transflective LCD disposed
2 between a first data line and a second data line, comprising:
3 a reflective cell comprising a first transistor and a
4 first reflective electrode, wherein the first
5 transistor comprises a gate coupled to a scan line,
6 a source coupled to the first data line, and a drain
7 coupled to the first reflective electrode, and the
8 first transistor is covered by the first reflective
9 electrode; and

10 a transmission cell comprising a second transistor and
11 a transparent electrode, wherein the second
12 transistor comprises a gate coupled to the scan line,
13 a source coupled to the second data line, and a drain
14 coupled to the transparent electrode, and the second
15 transistor is covered by a second reflective
16 electrode.

1 2. The pixel structure of transflective LCD of claim
2 1, wherein the second reflective electrode is a first reflective
3 electrode of another pixel structure.

1 3. The pixel structure of transflective LCD of claim
2 1, wherein the drain of the first transistor is coupled to
3 the first reflective electrode by a first plug, and the drain
4 of the second transistor is coupled to the transmission
5 electrode by a second plug.

1 4. The pixel structure of transflective LCD of claim
2 3, further comprising a first metal layer and a second metal

3 layer, acting as storage capacitors, disposed under the first
4 plug and the second plug respectively.

1 5. A pixel structure of transflective LCD disposed
2 between a first data line and a second data line, comprising:
3 a reflective cell comprising a first transistor and a
4 first reflective electrode, wherein the first
5 transistor comprises a gate coupled to a scan line,
6 a source coupled to the first data line, and a drain
7 coupled to the first electric electrode; and
8 a transmission cell comprising a second transistor and
9 a transparent electrode, wherein the second
10 transistor comprises a gate coupled to the second
11 line, a source coupled to the second data line, and
12 a drain coupled to the transparent electrode, and
13 the first transistor and the second transistor are
14 covered by the first reflective electrode.

1 6. The pixel structure of transflective LCD of claim
2 5, wherein the drain of the first transistor is coupled to
3 the first reflective electrode by a first plug, and the drain
4 of the second transistor is coupled to the transparent by a
5 second plug.

1 7. The pixel structure of transflective LCD of claim
2 6, further comprising a first metal layer and a second metal
3 layer, acting as storage capacitors, disposed under the first
4 plug and the second plug respectively.

1 8. The pixel structure of transflective LCD of claim
2 5, wherein the first data line and the second data line are
3 bent, the drain of the first transistor is coupled to the first

4 reflective electrode by a first plug, and the drain of the
5 second transistor is coupled to the transparent by a second
6 plug.

1 9. The pixel structure of transflective LCD of claim
2 8, further comprising a metal line disposed under the drains
3 of the first and second transistor, acting as a storage
4 capacitor.

1 10. The pixel structure of transflective LCD of claim
2 8, wherein the metal line is bent, and the metal line acting
3 as a storage capacitor is disposed under the drains of the
4 first and second transistor.

1 11. A pixel structure of transflective LCD disposed
2 between a first data line and a second data line, comprising:
3 a reflective cell comprising a first transistor and a
4 first reflective electrode, wherein the first
5 transistor comprises a gate coupled to a first scan
6 line, a source coupled to the first data line, and
7 a drain coupled to the first reflective electrode,
8 and the first transistor is covered by the first
9 reflective electrode; and
10 a transmission cell comprising a second transistor and
11 a transparent electrode, wherein the second
12 transistor comprises a gate coupled to a second scan
13 line, a source coupled to the first data line, and
14 a drain coupled to the transparent electrode, and
15 the second transistor is covered by a second
16 reflective electrode.

1 12. The pixel structure of transflective LCD of claim
2 11, wherein the second reflective electrode is a first
3 reflective electrode of another pixel structure.

1 13. The pixel structure of transflective LCD of claim
2 11, wherein the drain of the first transistor is coupled to
3 the first reflective electrode by a first plug, and the drain
4 of the second transistor is coupled to the transparent electrode
5 by a second plug.

1 14. The pixel structure of transflective LCD of claim
2 13, further comprising a first metal layer and a second metal
3 layer, acting as storage capacitors, disposed under the first
4 plug and the second plug respectively.

1 15. A pixel structure of transflective LCD disposed
2 between a first data line and a second data line, comprising:
3 a reflective cell comprising a first transistor and a
4 first reflective electrode, wherein the first
5 transistor comprises a gate coupled to a scan line,
6 a source coupled to the first data line, and a drain
7 coupled to the first reflective electrode; and
8 a transmission cell comprising a second transistor and
9 a transparent electrode, wherein the second
10 transistor comprises a gate coupled to a second scan
11 line, a source coupled to the first data line, and
12 a drain coupled to the transparent electrode, and
13 the first and second transistor are covered by the
14 first reflective electrode.

1 16. The pixel structure of transflective LCD of claim
2 15, wherein the drain of the first transistor is coupled to
3 the first reflective electrode by a first plug, and the second
4 transistor is coupled to the transparent electrode by a second
5 plug.

1 17. The pixel structure of transflective LCD of claim
2 16, further comprising a first metal layer and a second metal
3 layer, acting as storage capacitors, disposed under the first
4 plug and the second plug respectively.

1 18. The pixel structure of transflective LCD of claim
2 15, wherein the first transistor and the second transistor
3 are disposed on different sides under the transparent
4 electrode.

1 19. The pixel structure of transflective LCD of claim
2 16, further comprising a first metal layer acting as a storage
3 capacitor disposed under the first plug, and extending to an
4 underside of the drain of the second transistor.